



# QW1040 THRU QW10200

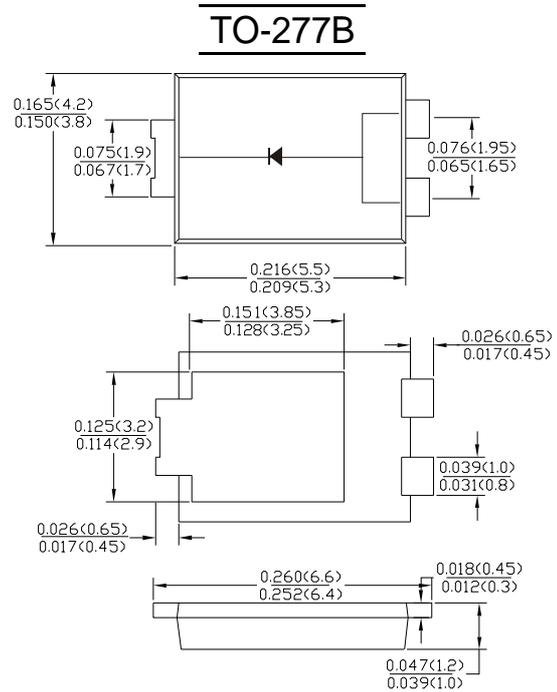
## Surface Mount Schottky Barrier Rectifiers

### Features

- Schottky Barrier Chip
- High Thermal Reliability
- Patented Super Barrier Rectifier Technology
- High Forward Surge Capability
- Ultra Low Power Loss, High Efficiency
- Excellent High Temperature Stability
- Plastic material-UL flammability 94V-0

### Mechanical Data

- Case: TO-277, molded plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Mounting Position: Any
- Marking: Type Number
- Lead Free: For RoHS/Lead Free Version



### Maximum Ratings and Electrical Characteristics @T<sub>A</sub> =25 °C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Symbol	QW1040	QW1045	QW1050	QW1060	QW1080	QW10100	QW10150	QW10200	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>									V
Working Peak Reverse Voltage	V <sub>RWM</sub>	40	45	50	60	80	100	150	200	V
DC blocking voltage	V <sub>DC</sub>									V
RMS Rectified Voltage	V <sub>R(RMS)</sub>	28	32	35	42	56	70	105	140	V
Average Rectified Output Current (Note1)	I <sub>F(AV)</sub>	10								A
Non-Repetitive Peak Forward Surge 8.3ms Single Half Sine-Wave Superimposed on rated load (JEDEC Method) (Note2)	I <sub>FSM</sub>	130								A
I <sup>2</sup> t Rating for Fusing (t < 8.3ms)	I <sup>2</sup> t	70.135								A <sup>2</sup> s
Forward Voltage Drop T <sub>A</sub> =25 °C @IF=5A	V <sub>FM</sub>	0.52		0.68		0.82		0.85	0.90	V
Peak Reverse Current T <sub>A</sub> =25 °C	I <sub>R</sub>	0.3								mA
At Rated DC Blocking Voltage T <sub>A</sub> =100 °C		15								
Typical Thermal Resistance Junction to Ambient	R <sub>θJA</sub> R <sub>θJL</sub>	80 15								°C/W
Operating junction temperature range	T <sub>J</sub>	-55 to +150								°C
storage temperature range	T <sub>STG</sub>	-55 to +150								°C

Note: 1. Valid Provided that are kept at ambient temperature at a distance of 9.5mm from the case.

2. Fr-4pcb. 2oz. Copper, minimum recommend pad layout .18.8mm×14.4. Anode pad dimensions 5.6mm×14.4mm.





**Characteristic Curves** ( $T_A=25\text{ }^\circ\text{C}$  unless otherwise noted)

Fig.1 - Forward Current Derating Curve

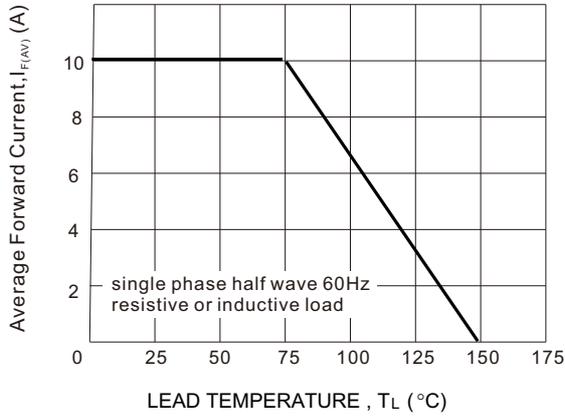


Fig2 : Instantaneous Forward Voltage

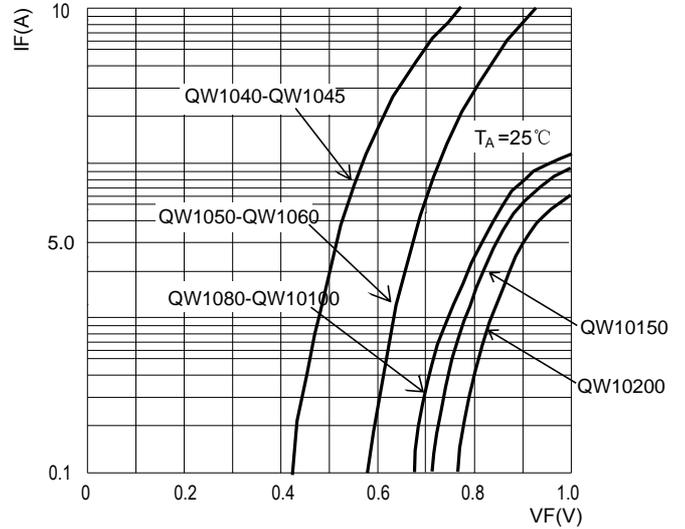


Fig3: Surge Forward Current Capability

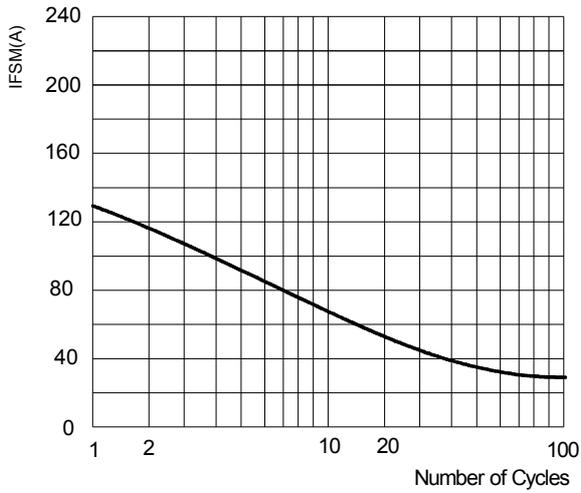


Fig4: Typical Reverse Characteristics

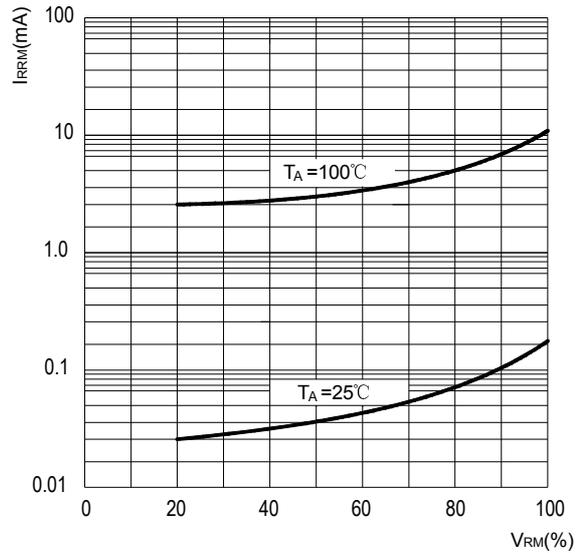


FIG.5 MOUNTING PAD LAYOUT

